NEW FEATURES INCLUDE:

- Near and farfield superposition based on circuit simulation
- Substructure: increased encryption security for read protected parts
- History and parameters in Boolean operations
- Substructure: simplified conformal mapping based on local coordinate system
- Improved EDA Import, SMD wizard
- MIMO antennas: envelope correlation coefficient

EMPIRE XPU 7.6
3D EM DESIGN SUITE

High performance 3D time domain EM modeling tool for Antennas, Microwave Circuits, EM Chip design and much more....

- Extremely fast and highly memory efficient using IMST proprietary XPU Technology
  - Full parallelisation on modern PCs (outperforms GPU supercomputers)
  - Just in time code generation and caching reduces required memory by 50%
- Interoperability with all common 3D CAD and layout formats and simulation vendor projects
- Intuitive 3D Design mode with fully integrated multilayer designer

SATCOM / 5G Digital beamforming frontend module: Electric field on signal path from PA to antenna
XPU TECHNOLOGY SURPASSES SIMULATION SPEED OF GPU CARDS FOR FDTD SIMULATIONS

Simulation speed using EMPIRE XPU vs. GPU FDTD

Simulation speed and size using

EMPIRE XPU on standard PCs vs.

FDTD on dual Xeon PC with one Nvidia Tesla K80 GPU card

APPLICATION EXAMPLES:

SATCOM / 5G Digital Beam Forming frontend module

Simulation time: < 2 h
Memory usage: 16 GB
Size: 600 Mcells
Dual Xeon workstation

UHF Satellite antenna

Simulation time: 6 min
Memory usage: 2 GB
Size: 44 Mcells
Dual Xeon workstation

EMPIRE XPU™ is a product of IMST GmbH
Carl-Friedrich-Gauss-Str. 2-4
47475 Kamp-Lintfort
Germany

Copyright© 2018 IMST GmbH. All rights reserved. Subject to technical changes without notice.